### **Precision Thick Film Chip Resistors Product Specification**

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#### 1 Scope:

- 1.1 This specification is applicable to lead and halogen free RTR series precision thick film chip resistors.
- 1.2 Lead free products mean lead free termination meets RoHS requirement. Pb contained in glass material of resistor element are exempted by RoHS directive.

#### 2 Explanation Of Part Numbers:

(EX) 1002 <u>RTR</u> <u>TH</u> Size (Refer to 3. Resistance Packaging Type Nominal Resistance General (Refef to Packing Specification) Tolerance Specifications) 01(0201) TH: 2 mm Pitch Carrier Tape 10000 pcs  $B = \pm 0.1\%$ EX. 10.2Ω=10R2 Precision Thick Film 4- $D=\pm 0.5\%$ Chip Rsisitors Digit 10Κ $\Omega$ =1002F=± 1%

#### 3 General Specifications:

3.1 Resistance Range:  $\geq 1\Omega$ 

	Rated	Max.	Max.		Resista	nce Range
Туре	Power at 70℃	Working Voltage	Overload Voltage	T.C.R (ppm/°C)	B(±0.1%) \ D(±0.5%) E-24 \ E-96	F(±1.0%) E-24 <b>\ E-96</b>
RTR01 (0201)	1/20 W	30V	60V	±100	$10\Omega \leq R \leq 1M\Omega$	$10\Omega {\le} R {\le} 1M\Omega$
RTR02	1 16W	50V	100V	±50	$100\Omega {\le} R {\le} 1M\Omega$	100Ω≦R≦1MΩ
(0402)	16 <sup>vv</sup>	30 V	1007	±100		1Ω≦R<10Ω
RTR03	1/10 W	75V	150V	±50	$100\Omega {\le} R {\le} 1M\Omega$	100Ω≦R≦1MΩ
(0603)	10 **	750	1307	±100		1Ω≦R<10Ω
RTR05	1 ,,,	1/8 W 150V	300/	±50	$100\Omega {\le} R {\le} 1M\Omega$	$100\Omega \le R \le 1M\Omega$
(0805)	8		300V	±100		1Ω≦R<10Ω
RTR06	1 4 W	200V	400V	±50	$100\Omega {\le} R {\le} 1M\Omega$	$100\Omega \le R \le 1M\Omega$
(1206)	4	200 V	4000	±100		1Ω≦R<10Ω
RTR12	1 2 W	200V	400V	±50	$100\Omega {\le} R {\le} 1M\Omega$	$100\Omega \le R \le 1M\Omega$
(1210)		200 V	4000	±100		1Ω≦R<10Ω
RTR20	3 4 W	200V	400V	±50	$100\Omega {\le} R {\le} 1M\Omega$	100Ω≦R≦1MΩ
(2010)	4	2007	4007	±100		1Ω≦R<10Ω
RTR25	1W	200V	400V	±50	$100\Omega {\le} R {\le} 1M\Omega$	100Ω $\le$ R $\le$ 1MΩ
(2512)	(2512)	200 V	7000	±100		1Ω≦R<10Ω
Opera	ating Tem	perature F	Range		-55℃ ~ +155	5℃

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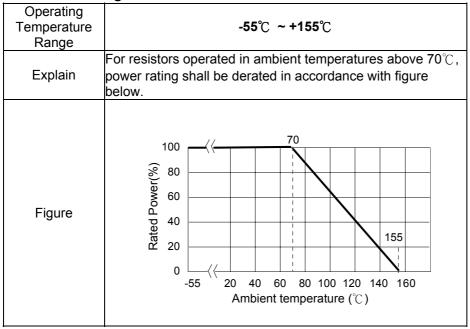
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3.2 Resistance Range:  $< 1\Omega$ 

Туре	Rated Power at 70℃	Max. Rated Current	Max. Overload Current	T.C.R (ppm / °C)	Resistance Range F(±1%) E-96
RTR05 (0805)	1 8	0.83A	2.08A	±100	150 mΩ $\leq$ R $<$ 1Ω
RTR06 (1206)	<u>1</u> W	1.29A	1.94A	±100	150 mΩ $\leq$ R $<$ 1Ω
RTR20 (2010)	3 4 W	2.50A	6.25A	±100	120 mΩ≦R<1Ω
RTR25 (2512)	1W	2.89A	7.22A	±100	120 mΩ≦R<1Ω
	Operating	Temperature Ra	ange		-55℃ ~ +155℃

#### 3.3 Power Derating Curve:



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3.4 Voltage Rating or Current Rating

3.4.1 Resistance Range:  $\ge 1\Omega$ 

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

 $E = \sqrt{R \times P}$ 

E= Rated voltage (v)

P= Power rating (w)

R= Nominal resistance( $\Omega$ )

3.4.2 Resistance Range:  $< 1\Omega$ 

Rated Current: The resistor shall have a DC continuous working current or a rms. AC continuous working current at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

 $I = \sqrt{P/R}$ 

I= Rated current (A)

P= Power rating (w)

R= Nominal resistance( $\Omega$ )

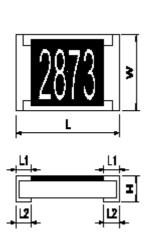
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#### 4 Dimensions:

4.1 Resistance Range:  $\ge 1\Omega$ 

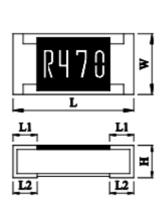
Unit:mm



_						Unit:mm
Туре	Dimension Size Code	L	W	н	L1	L2
RTR01	0201	0.60±0.03	0.30±0.03	0.23±0.03	0.15±0.05	0.15±0.05
RTR02	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
RTR03	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
RTR05	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
RTR06	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
RTR12	2010	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
RTR20	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
RTR25	2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

4.2 Resistance Range:  $< 1\Omega$ 

Unit:mm



						Unit:mm
Туре	Dimension Size Code	-	W	н	L1	L2
RTR05	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.20
RTR06	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.65±0.15
RTR20	2010	5.00±0.10	2.50±0.10	0.60±0.10	0.65±0.20	0.65±0.20
RTR25	2512	6.40±0.20	3.20±0.10	0.60±0.10	0.65±0.20	0.65±0.20

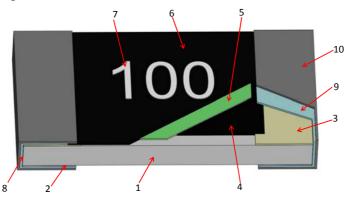
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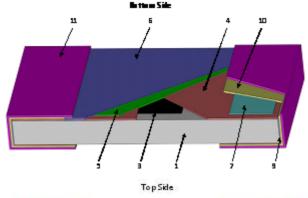
### 5 Structure Graph:

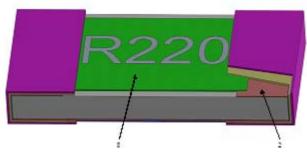
#### 5.1 Resistance Range: $\geq 1\Omega$



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

### 5.2 Resistance Range: $< 1\Omega$





1	Ceramic substrate	7	C3 layer
2	Top inner electrode	8	G2 layer+Marking
3	Resistive layer	9	Terminal inner electrode
4	Bottom inner electrode	10	Ni plating
5	1st Protective coating	11	Sn plating
6	2nd Protective coating		

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### 6 Reliability Test:

### 6.1 Electrical Performance Test

Item	Conditions	Specifications
пеш	Conditions	Resistors
Temperature Coefficient of Resistance	TCR (ppm/°C) = $\frac{(R2-R1)}{R1 (T2-T1)} \times 10^6$ R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C Refer to JIS-C5201-1 4.8	Refer to item 3. general specifications
Short Time Overload	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications)  Refer to JIS-C5201-1 4.13	
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material.  Refer to JIS-C5201-1 4.6  Metal black measuring plate measuring point B measuring p	
Dielectric Withstand Voltage	Put the resistor in the fixture, add VAC (see spec. below) in +,-terminal for.  RTR05 \ 06 \ 12 \ 20 \ 25 apply 500 VAC 1 minute.  RTR01 \ 02 \ 03 apply 300 VAC 1 minute.  Refer to JIS-C5201-1 4.7	No short or burned on the appearance.
Intermittent Overload	Put the tested resistor in chamber under temperature 25±2°C and load the rated DC voltage for 1 sec on , 25 sec off , 10000 +400 test cycles, then it be left at no-load for 1 hour , then measure its resistance variance rate.  Refer to JIS-C5201-1 4.13	ΔR%=±(5.0%+0.10Ω)
Noise Level	Refer to JIS-C5201-1 4.12	$\begin{array}{c cccc} Resistance & Noise \\ \hline R < 100 \Omega & \leq & -10db & (0.32 \text{ uV/V}) \\ 100 \Omega & \leq R < 1K \Omega & \leq & 0db & (1.0 \text{ uV/V}) \\ 1K \Omega & \leq R < 10K \Omega & \leq & 10db & (3.2 \text{ uV/V}) \\ 10K \Omega & \leq R < 100K \Omega & \leq & 15db & (5.6 \text{ uV/V}) \\ 100K \Omega & \leq R < 1M \Omega & \leq & 20db & (10 \text{ uV/V}) \\ 1M \Omega & \leq R & \leq & 30db & (32 \text{ uV/V}) \\ \hline \end{array}$

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# **Precision Thick Film Chip Resistors Product Specification**

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#### 6.2 Mechanical Performance Test

Item	Conditions	Specifications Resistors
Body Strength	2.RTR05 \ 06 \ 12 \ 20 \ 25 : probe R0.5	
	L	No side conductive peeling off.
Terminal Strength	Test1:The resistor mounted on the board applied 5N pushing force on the sample rear for 10 sec. (RTR01:3N) Test2:The resistor mounted on the board slowly add force on the sample rear until the sample termination is breakdown. Refer to JIS-C5201-1 4.16	damage. Test2:RTR01≧3N
Resistance to Solvent	The tested resistor be immersed into isopropyl alcohol of 20~25℃ for 5 minutes, then the resistor is left in the room for 48 hrs, and	
		No evidence of mechanical damage, No G2 overcoating and Sn layer by leachin
Solderability	Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of 1.22×10⁵ Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature 235±5°C for 2 sec, then the resistor is left as placed under microscope to observe its solder area. Refer to JIS-C5201-1 4.17	
Resistance to Soldering Heat	Preheating temperature: $350\pm10^{\circ}$ C  Electric iron preheating time: $3+1/-0$ sec  Preheating the electric iron on electrode termination, as after that step placed the iron over 60 min. and measured its resistance variance rate.	1.Resistance Range: ≥ 1Ω
	Refer to JIS-C5201-1 4.18	2.Resistance Range:<1 Ω

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Item	Conditions	Specifications
Joint Ctronath	Droonditioning:	Resistors Test item 1:
Joint Strength of Solder	Preconditioning: Put tested resistor in the apparatus of PCT, at a temperature of	
oi soidei	105°C, humidity of 100% RH, and pressure of 1.22×10 $^5$ Pa for a	
	duration of 4 hours. Then after left the specimen in a temperature	· · · · · · · · · · · · · · · · · · ·
	for 2 hours or more.	2.Resistance Range:<1 $\Omega$
	Test method:	$\triangle$ R%=±(1.0%+0.001 $\Omega$ )
	⊚Test item 1(Adhesion):	(2).No evidence of mechanical damage
	A static load using a R0.5 (RTR01:R0.1) scratch tool shall be	No terminal peeling off.
	applied on the core of the component and in the direct ion	
	of the arrow and held for 10 seconds and under load	
	measured its resistance variance rate.	(1).Vaviance rate on resistance
	Load: 1.RTR01=5N	1.Resistance Range:≧1Ω
	2.RTR02=10N	$\triangle$ R%=±(1.0%+0.05 $\Omega$ )
	3.Other type=20N  Cross-sectional view	2.Resistance Range:<1 Ω
	Scratching jig	$\triangle R\% = \pm (1.0\% + 0.001 \Omega)$
		(2).No evidence of mechanical damage
		No terminal peeling off and core bod
	R 0.5	cracked.
		ordonod.
	Specimen	
	fer to JIS-C5201-1 4.32	
	○Test item 2 (Bending Strength):	
	Solder tested resistor on to PC board. Add force in the middle	
	down, and under load measured its resistance variance rate.	
	D:RTR02 \ 03 \ 05=5mm	
	RTR01 \ 06 \ 12=3mm	
	RTR20 · 25=2mm	
	Resistor Testing circuit board	
	Nesistal Transfer of the Control of	
	Solder Supporting tig	
	45	
	Chip registor	
	. 1201	
	200 - 100 -	
	Pressurtze	
	[&==:	
	D (Amount of band)	
	OHM Meter	
	Refer to JIS-C5201-1 4.33	
Vibration	The resistor shall be mounted by its terminal leads to the	_
	supporting terminals on the solid table.	1%:±(0.5%+0.05Ω)
	The entire frequency range: from 10Hz to 55Hz and return to 10Hz,	2% · 5%:±(1.0%+0.05Ω)
	shall be transferred in 1 min.	2.Resistance Range : <1 Ω
	Amplitude:1.5mm	1%、2%、5%:±(1.0%+0.001Ω)
	This motion shall be applied for a period of 2 hours in each 3	
		No evidence of mechanical damage
	Refer to JIS-C5201-1 4.22	1

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#### 6.3 Environmental Test

Item	Conditions		Specifications Resistors
Resistance to	Put tested resistor in chamber under temperature 155±5°C for		
Dry Heat	1000 +48/-0 hours. Then leaving the tested resistor in room		_
•	temperature for 60 minutes, and measure its resistance variance		
	rate.		ΔR%=±(1.0%+0.001Ω)
	Refer to JIS-C5201-1 4.25		No evidence of mechanical damage.
Thermal Shock	Put the tested resistor in the chamber und	der the Thermal Sh	
	which shown in the following table shall be repeated 300 times		mes AR%=:+(0.5%+0.050)
	consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate.		oom 2.Resistance Range:<1Ω
			nce $\Delta$ R%=±(1.0%+0.001Ω)
	Testing Condition		No evidence of mechanical damage.
	Lowest Temperature	-55±5℃	
	Highest Temperature	<b>125±5</b> ℃	
		5 minutes each	
	Refer to MIL-STD 202 Method 107		
Loading Life	Put the tested resistor in the chamber under	•	·
in Moisture	relative humidity 90~95% and load the		
	minutes on, 30 minutes off, total 1000 ho	the Other Type : $\triangle R\% = \pm (0.5\% + 0.05\Omega)$	
	tested resistor in room temperature for 60		
	its resistance variance rate. Refer to JIS-C5201-1 4.24	$\Delta R\% = \pm (2.0\% + 0.001\Omega)$	
			No evidence of mechanical damage.
Load Life	Put the tested resistor in chamber under	•	•
	and load the rated voltage for 90 minute		
	total 1000 hours. Then leaving the tes		
	temperature for 60 minutes, and med	2.1 Coloration Range. 1122	
	variance rate.	$\Delta R\% = \pm (2.0\% + 0.001\Omega)$	
	Refer to JIS-C5201-1 4.25		No evidence of mechanical damage.
Low	Put the tested resistor in the chamber at	room temperature	25 1.Resistance Range:≧1Ω
Temperature	°C .Decreasing the temperature to -55	the $\triangle R\% = \pm (0.5\% + 0.05\Omega)$	
Operation	temperature at -55°C for 1 hour. Then load	for 2.Resistance Range:<1Ω	
	45 minutes on, and 15 minutes off. Then leaving the tested		red $\triangle R\% = \pm (1.0\% + 0.001\Omega)$
	resistor in room temperature for 8±1 hours, and measure its		its No evidence of mechanical damage.
	resistance variance rate.		ino evidence of mechanical damage.
	Refer to MIL-R-55342D 4.7.4		
Whisker Test	⊚Test item 1 (Thermal Shock test):		Max. 50µm
	Testing Condition		
	Minimum storage temperature	-55+0/-10℃	
	Maximum storage temperature	85+10/-0℃	
	Temperature-retaining time	10 min.	
	Number of temperature cycles	1,500	
	⊙Inspection:		
	Inspect for whisker formation on specime		
	acceleration test specified in subciause		
	(stereo microscope) of about 40 or hi		
	judgment is hard in this method, use		ron
	microscope (SEM) of about 1,000 or highe	r magnification.	
	By JEDEC Standard NO.22A121 class 2.		

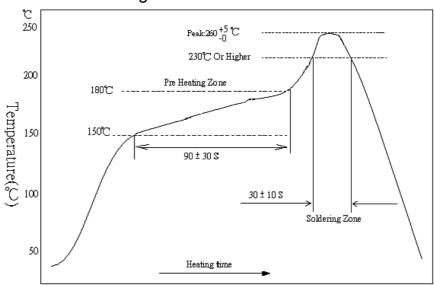
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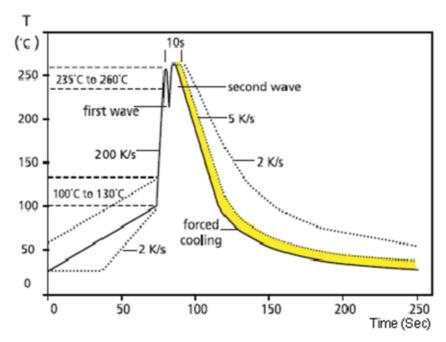
#### 7 Recommend Soldering Method:

7.1 Lead Free IR Reflow Soldering Profile



Remark: The peak temperature of soldering heat is 260 +5/-0 °C for 10 seconds

7.2 Lead Free Double-Wave Soldering Profile.(This applies to 0603 size inclusive above products )



7.3 Soldering Iron: temperature 350°C±10°C , dwell time shall be less than 3 sec.

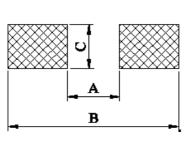
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#### 8 Recommend Land Pattern Design (For Reflow Soldering):

Unit:mm



TYPE DIM	А	В	С
RTR01	0.3	1.0	0.4
RTR02	0.5	1.5	0.6
RTR03	8.0	2.1	0.9
RTR05	1.2	3.0	1.3
RTR06	2.2	4.2	1.6
RTR12	2.2	4.2	2.8
RTR20	3.5	6.1	2.8
RTR25	3.8	8.0	3.5

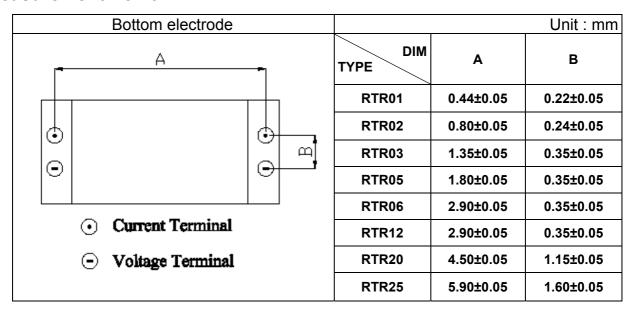
#### 9 Plating Thickness:

9.1 Ni: $\ge$ 1  $\mu$  m

9.2 Sn(Tin): $\ge$ 3  $\mu$  m

9.3 Sn(Tin):Matte Sn

#### 10 Measurement Point:



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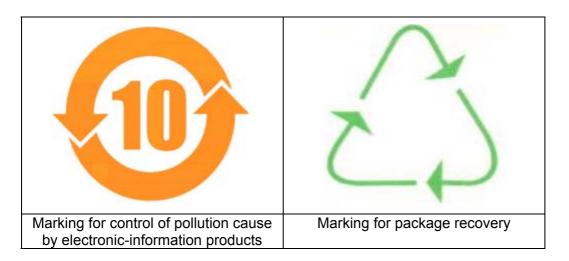
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#### 11 Stock period:

11.1 The temperature condition must be controlled at 25±5°C, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.

### 12 The carton packaged for electronic-information products is made by the symbol as follows: (For china)



#### 13 Attachments:

13.1 Document Revise Record Paper (QA-QR-027)

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