Thick Film Chip Resistors Product Specification

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1 Scope:

- 1.1 This specification is applicable to lead and halogen free RTT series thick film chip resistors.
- 1.2 Lead free products mean lead free termination meets RoHS requirement. Pb contained in glass material of resistor element are exempted by RoHS directive.
- 1.3 The product is for general purpose.
- 1.4 The available AEC-Q200 report also can provide by customer request.

2 Explanation Of Part Numbers:

(EX) **RTT** 100 <u>TH</u> Resistance Size Nominal Resistance Packaging (Refer to IE-SP-055) Type Tolerance 01(0201) TH: 2 mm Pitch Carrier Tape 10000 pcs EX. 10Ω = 10002(0402) $4.7\Omega = 4R7$ 03(0603) $B = \pm 0.1\%$ Digit JUMPER=000 05(0805) $D=\pm 0.5\%$ Thick Film 06(1206) F=± 1% Chip Resistors 12(1210) EX. 10.2Ω=10R2 G=± 2% 18(1812) J=± 5% 10KΩ=1002 Digit 20(2010) BA: Bulk Case JUMPER=0000 25(2512)

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3 General Specifications:

3.1 Resistance Range: \geq 1 Ω & 0 Ω

Туре	Rated Power at	Max. Working			Max. Overload	T.C.R (ppm/°C)		Resistan	ce Range		Ra	PER ted rent	JUM Resis Va	
	70 ℃	Voltage	Voltage	(ppiii/C)	B(±0.1%) E-24 · E-96	D(±0.5%) E-24 \ E-96	F(±1%) E-24 \ E-96	G(±2%) \ J(±5%) E-24	J (±5%)	F (±1%)	J (±5%)	F (±1%)		
RTT01	1 20 W	25V	50V	-200 +400		$1\Omega \le R < 10\Omega$	1Ω≦R<10Ω	1Ω≦R<10Ω	0.5A	0.5A	50mΩ	35mΩ		
(0201)	20			±200	$47\Omega {\le} R {\le} 1M\Omega$	$10\Omega {\le} R {\le} 10M\Omega$	$10\Omega {\le} R {\le} 10M\Omega$	$10\Omega {\le} R {\le} 10M\Omega$			MAX.	MAX.		
RTT02	W	50V	100V	±100	$100\Omega {\le} R {\le} 1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 22M\Omega$	$10\Omega {\le} R {\le} 22M\Omega$	1A		50mΩ	20mΩ		
(0402)	16	500	100 V	±200			$1\Omega {\le} R {<} 10\Omega$	1Ω≦R<10Ω	IA	1.5A	MAX.	MAX.		
RTT03	1 ,,,	75)/	150V	±100	$100\Omega {\le} R {\le} 1M\Omega$	10Ω≦R≦1MΩ	$10\Omega {\le} R {\le} 22M\Omega$	10Ω≦R≦22MΩ	4.0		50mΩ	20mΩ		
(0603)	10	10 /5V 1	10 75V 150	1500	±200		1Ω≦R<10Ω	1Ω≦R<10Ω	1Ω≦R<10Ω	1A	2A	MAX.	MAX.	
RTT05	W	450)/	2001/	±100	$100\Omega {\le} R {\le} 1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 10M\Omega$	$10\Omega {\le} R {\le} 27 M\Omega$	10Ω≦R≦27MΩ				20mΩ MAX.		
(0805)	8 VV	150V	300V	±200		1Ω≦R<10Ω	1Ω≦R<10Ω	1Ω≦R<10Ω	2A					
RTT06	1 ,,,	200V	400V	±100	10Ω≦R≦1MΩ	$10\Omega \! \leq \! R \! \leq \! 10M\Omega$	10Ω≦R≦27MΩ	10Ω≦R≦27MΩ	24	2.54	50mΩ	20mΩ		
(1206)			2000 4000	±200	3Ω≦R<10Ω	1Ω≦R<10Ω	1Ω≦R<10Ω	1Ω≦R<10Ω	2A	3.5A	MAX.	MAX.		
RTT12	1 ,,,	2001/	400)/	±100	$100\Omega {\le} R {\le} 1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 10M\Omega$	10Ω≦R≦27MΩ	10Ω≦R≦27MΩ	2.4	4.0	50mΩ	20mΩ MAX.		
(1210)	1 W	200V	400V	±200			1Ω≦R<10Ω	1Ω≦R<10Ω	2A	4A	MAX.			
RTT18	3_W	0001/	400)/	±100	$100\Omega {\le} R {\le} 1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 10M\Omega$	$10\Omega {\le} R {\le} 20M\Omega$	10Ω≦R≦20MΩ	0.4			20mΩ MAX.		
(1812)	4	2007	200V 400V	±200			1Ω≦R<10Ω	1Ω≦R<10Ω	2A	5A				
RTT20	3 4 W	0001/	400)/	±100	$100\Omega {\le} R {\le} 1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 10M\Omega$	$10\Omega {\le} R {\le} 20M\Omega$	$10\Omega {\le} R {\le} 20M\Omega$	0.4		50mΩ	20mΩ MAX.		
(2010)	4 VV	200V	200V 400V	±200			1Ω≦R<10Ω	1Ω≦R<10Ω	2A	5A	MAX.			
RTT25	4144	0001/	400)/	±100	$100\Omega {\le} R {\le} 1M\Omega$	$10\Omega\!\leq\!R\!\leq\!10M\Omega$	$10\Omega \! \leq \! R \! \leq \! 20M\Omega$	$10\Omega {\le} R {\le} 20M\Omega$	0.4	7.0	50mΩ	20mΩ		
(2512)	100	1W 200V	400V	±200			1Ω≦R<10Ω	1Ω≦R<10Ω	2A	7A	MAX.	MAX.		
Oper	ating Tem	perature	Range			-55°C ~ +	155℃ (0201:-	-55°C ~ +125°	C)					

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3.2 Resistance Range: $< 1\Omega$

	Data d Danna			TOD	Resistance Range
Type	Rated Power at 70℃	Max. Rated Current	Max. Overload Current	T.C.R (ppm / °C)	F(±1%) \ G(±2%) \ J((±5%) E-24 \ E-96
				±1500	25 mΩ \leq R $<$ 37 mΩ
				±1200	37 mΩ \leq R $<$ 60 mΩ
RTT02	4/40\\	4.504	2.054	±600	60 mΩ \leq R $<$ 200 mΩ
(0402)	1/16W	1.58A	3.95A	±300	200 mΩ \leq R $<$ 400 mΩ
				±250	400 mΩ \leq R $<$ 600 mΩ
				±200	600 mΩ \leq R $<$ 1000 mΩ
				±1500	10 mΩ \leq R $<$ 37 mΩ
				±1200	37 m $\Omega{\le}R{<}60$ m Ω
RTT03	4/40\\	2.464	7.04.4	±600	60 mΩ≦R<100 mΩ
(0603)	1/10W	3.16A	7.91A	±300	100 mΩ \leq R $<$ 200 mΩ
				±600	$200 \text{ m}\Omega \leq R < 500 \text{ m}\Omega$
				±400	$500 \text{ m}\Omega \leq R < 1000 \text{ m}\Omega$
				±1500	10 mΩ \leq R $<$ 19 mΩ
				±1200	19 mΩ \leq R $<$ 33 mΩ
RTT05	1/8W	3.53A	8.82A	±800	33 mΩ \leq R $<$ 50 mΩ
(0805)				±600	50 mΩ \leq R $<$ 100 mΩ
				±200	100 mΩ \leq R $<$ 1000 mΩ
				±1500	10 mΩ≦R<19 mΩ
				±1200	19 mΩ \leq R $<$ 25 mΩ
RTT06	1/3W	5.77A	14.42A	±1000	25 mΩ≤R<50 mΩ
(1206)	.,, 611	3		±600	50 mΩ≦R<100 mΩ
				±200	 100 mΩ≦R<1000 mΩ
				±1500	10 mΩ≦R<19 mΩ
				±1000	 19 mΩ≦R<25 mΩ
RTT12	1/2W	7.07A	17.67A	±700	 25 mΩ≤R<50 mΩ
(1210)				±400	50 mΩ≦R<100 mΩ
			-	±200	 100 mΩ≦R<1000 mΩ
				±1500	10 mΩ≦R<19 mΩ
				±1200	19 mΩ≦R<25 mΩ
RTT18				±900	25 mΩ≦R<50 mΩ
(1812)	3/4W	8.66A	21.65A	±500	50 mΩ≦R<100 mΩ
,			-	±200	100 mΩ≦R<1000 mΩ
			-	±200	100 mΩ≦R<1000 mΩ
				±1500	10 mΩ≤R<19 mΩ
				±1200	$19 \text{ m}\Omega \leq \text{R} < 25 \text{ m}\Omega$
RTT20	3/4W	8.66A	21.65A	±900	25 mΩ≤R<50 mΩ
(2010)	J. 144	0.0071	250/1	±500	$50 \text{ m}\Omega \leq R < 100 \text{ m}\Omega$
				±200	100 mΩ≤R<1000 mΩ
				±1500	10 mΩ≤R<19 mΩ
				±1200	$19 \text{ m}\Omega \leq \text{R} < 25 \text{ m}\Omega$
RTT25	1 W	10A	25A	±900	$\frac{15 \text{ m}\Omega \leq R < 25 \text{ m}\Omega}{25 \text{ m}\Omega \leq R < 50 \text{ m}\Omega}$
(2512)	1 VV	10/4	20/1	±500	$50 \text{ m}\Omega \leq R < 100 \text{ m}\Omega$
` '				エンフロ	
. ,				±200	 100 mΩ≦R<1000 mΩ

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3.3 Power Derating Curve:

Туре	RTT01 (0201)	Other		
Operating Temperature Range	-55℃ ~ +125℃	-55°C ~ +155°C		
Explain	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.		
Figure	100 70 80 80 60 40 40 40 60 80 100 120 140 160 Ambient temperature (°C)	100		

3.4 Voltage Rating or Current Rating

3.4.1Resistance Range: $\geq 1\Omega$

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following

$$E = \sqrt{R \times P}$$
 E= Rated voltage (v)

P= Power rating (w)

R= Nominal resistance(Ω)

3.4.2Range: $< 1\Omega$

Rated Current: The resistor shall have a DC continuous working current or a rms. AC continuous working current at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$I = \sqrt{P/R}$$
 I= Rated current (A)
P= Power rating (w)

R= Nominal resistance(Ω)

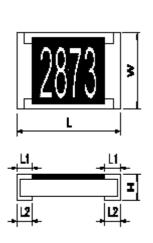
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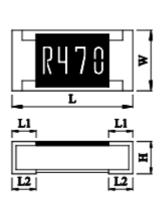
4 Dimensions:

4.1 Resistance Range: \geq 1 Ω & 0 Ω



						Unit:mm
Туре	Dimension Size Code	L	W	н	L1	L2
RTT01	0201	0.60±0.03	0.30±0.03	0.23±0.03	0.15±0.05	0.15±0.05
RTT02	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
RTT03	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
RTT05	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
RTT06	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
RTT12	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
RTT18	1812	4.40±0.20	3.15±0.20	.0.47±0.20	0.60±0.20	0.60±0.01
RTT20	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
RTT25	2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

4.2 Resistance Range: < 1 Ω



						Unit:mm
T	Dimension	L	W	Н	L1	L2
Type RTT02	Size Code \ 0402	1.00±0.10	0.50±0.05	0.30±0.10	0.25±0.10	0.20±0.15
RTT03	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.25±0.15	0.35±0.15
RTT05	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.20
RTT06	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.55±0.25
RTT12	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
RTT18	1812	4.40±0.20	3.15±0.20	.0.47±0.20	0.60±0.20	0.60±0.01
RTT20	2010	5.00±0.20	2.50±0.20	0.60±0.10	0.65±0.20	0.65±0.20
RTT25	2512	6.30±0.20	3.20±0.20	0.60±0.10	0.65±0.20	0.65±0.20

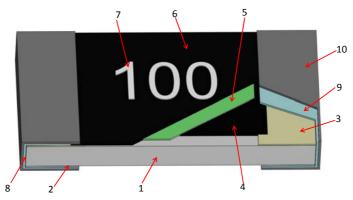
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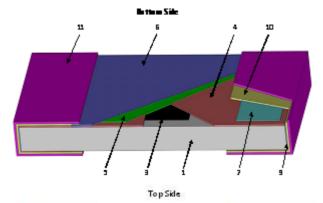
5 Structure Graph:

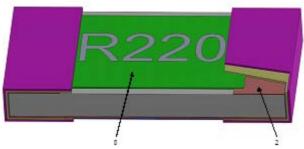
5.1 Resistance Range: $\geq 1\Omega \& 0 \Omega$



1	Ceramic substrate	6	2nd Protective coating	
2	Bottom inner electrode	ectrode 7 Marking		
3	Top inner electrode	8	Terminal inner electrode	
4	Resistive layer	9	Ni plating	
5	1st Protective coating	10	Sn plating	

5.2 Resistance Range: $< 1\Omega$





1	Ceramic substrate	7	2nd Top inner electrode
2	1st Top inner electrode	8	G2 layer+Marking
3	Resistive layer	9	Terminal inner electrode
4	Bottom inner electrode	10	Ni plating
5	1st Protective coating	11	Sn plating
6	2nd Protective coating		

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6 Reliability Test:

6.1 Electrical Performance Test

Item	Conditions	Specifications		
		Resistors	Jumper	
emperature Coefficient of Resistance	TCR (ppm / °C) = $\frac{(R2-R1)}{R1(T2-T1)} \times 10^6$ R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C	Refer to item 3. general specifications	NA	
Short Time Overload	Refer to JIS-C5201-1 4.8 Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications) Jumper : Applied Maximum overload current Type RTT01 RTT02 RTT03 RTT05 RTT06 RTT12 RTT18 RTT20 RTT25 (0201) (0402) (0603) (0805) (1206) (1210) (1812) (2010) (2512) ±5% 1.25A 2.5A 2.5A 5A 5A 5A 5A 5A 5A 5A	$ \begin{array}{c} \text{1.Resistance Range:} \geqq 1\Omega \\ \text{0.1\%} & \text{0.5\%} & \text{1\%:} \pm (1.0\% + 0.05\Omega) \\ \text{2\%} & \text{5\%:} \pm (2.0\% + 0.10\Omega) \\ \text{2.Resistance Range:} < 1\Omega \\ \text{1\%} & \text{2\%} & \text{5\%:} \pm (2.0\% + 0.001\Omega) \\ \\ \text{No evidence of mechanical damage.} \\ \text{No short or burned on the appearance.} \\ \end{array} $	Refer to ite 3. general specificatio	
	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material. Refer to JIS-C5201-1 4.6 Metal block measuring point B insulating plate measuring point B insulating enclosure surface Ro.5mm	$\geq 10^{9}\Omega$		
Dielectric Withstand Voltage	Put the resistor in the fixture, add VAC (see SPEC below) in +,- terminal for. RTT05 \cdot 06 \cdot 12 \cdot 18 \cdot 20 \cdot 25 apply 500 VAC 1 minute. RTT01 \cdot 02 \cdot 03 apply 300 VAC 1 minute. Refer to JIS-C5201-1 4.7	No short or burned on the appearance.		
Intermittent Overload	Put the tested resistor in chamber under temperature $25\pm2^{\circ}$ C and load 2.5 times rated DC voltage for 1 sec on, 25 sec off, 10000^{+400} test cycles, then it be left at no-load for 1 hour , then measure its resistance variance rate. Jumper : Applied Maximum overload current $\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$ \begin{array}{l} \text{1.Resistance Range:} \geqq 1\Omega \\ \pm (5.0\% + 0.10\Omega) \\ \text{2.Resistance Range:} < 1\Omega \\ \pm (5.0\% + 0.001\Omega) \\ \text{No evidence of mechanical damage.} \\ \text{No short or burned on the appearance.} \\ \end{array} $	Refer to ite 3. general specificatio	
Noise Level	Refer to JIS-C5201-1 4.12	$\begin{array}{ c c c c }\hline Resistance & Noise \\\hline R < 100\Omega & \leq & -10db & (0.32 \text{ uV/V}) \\\hline 100\Omega \leq R < 1K\Omega & \leq & 0db & (1.0 \text{ uV/V}) \\\hline 1K\Omega \leq R < 10K\Omega & \leq & 10db & (3.2 \text{ uV/V}) \\\hline 10K\Omega \leq R < 100K\Omega & \leq & 15db & (5.6 \text{ uV/V}) \\\hline 100K\Omega \leq R < 1M\Omega & \leq & 20db & (10 \text{ uV/V}) \\\hline 1M\Omega \leq R & \leq & 30db & (32 \text{ uV/V}) \\\hline \end{array}$	NA	

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6.2 Mechanical Performance Test

Item	Conditions	Specifications Resistors	lumnor
Body Strength	Applied R0.5 test probe at its central part then pushing 10N { 1.02 Kgf } force on the sample for 10 sec. 1.RTT02 \ RTT03 : probe R0.2 2.RTT05 \ 06 \ 12 \ 18 \ 20 \ 25 : probe R0.5	Resistors 1.Resistance Range: $\geq 1\Omega$ ±(1.0%+0.05Ω) 2.Resistance Range:<1Ω ±(1.0%+0.001Ω)	Jumper Refer to item 3. general specifications
	Refer to JIS-C5201-1 4.15	No evidence of mechanical damage. No side conductive peeling off	
Terminal Strength	Test 1: The resistor mounted on the board applied 5N pushing force on the sample rear for 10 sec. (RTT01:3N) Test 2: The resistor mounted on the board slowly add force on the sample rear until the sample termination is breakdown. Refer to JIS-C5201-1 4.16	Test 1 : No evidence of mechanical d Test 2 : RTT01≥3N Other Type≥5N	amage.
	The tested resistor be immersed into isopropyl alcohol of 20~25°C for 5 minutes, then the resistor is left in the room for 48 hrs, and measured its resistance variance rate.	2.Resistance Range:<1Ω ±(1.0%+0.001Ω)	Refer to item 3. general specifications
	Refer to JIS-C5201-1 4.29	No evidence of mechanical damage. No G2 overcoating and Sn layer by le	aching.
·	Preconditioning Put the tested resistor in the apparatus of PCT, at a temperature of 105℃, humidity of 100% RH, and pressure of 1.22×105 Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature 235±5℃ for 2 sec, then the resistor is left as placed under microscope to observed its solder area. Refer to JIS-C5201-1 4.17	Solder coverage over 95%	
Soldering Heat		Test item 1: (1).Variance rate on resistance 1.Resistance Range: ≥ 1Ω ΔR%=±(1.0%+0.05Ω) 2.Resistance Range:<1Ω ΔR%=±(1.0%+0.001Ω) (2).No evidence of electrode damage. No side conductive peeling off. Test item 2: (1).Solder coverage over 95%. (2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode. Test item 3: (1).Variance rate on resistance 1.Resistance Range:≥1Ω ΔR%=±(1.0%+0.05Ω) 2.Resistance Range:<1Ω ΔR%=±(1.0%+0.001Ω) (2).No evidence of electrode	Refer to item 3. general specifications
	Refer to JIS-C5201-1 4.18	damage. No side conductive peeling off.	

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Item	Conditions	Specifications	
		Resistors	Jumper
loint Strength of Solder	Preconditioning Put tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of 1.22×105 Pa for a duration of 4 hours. Then after left the specimen in a temperature for 2 hours or more. Test method: ③Test item 1 (Adhesion): A static load using a R0.5 (0201:R0.1) scratch tool shall be applied on the core of the component and in the direction of the arrow and held for 10 seconds and under load measured its resistance variance rate. Load:1.RTT01=5N 2.RTT02=10N 3.Other type=20N Cross-sectional view Secondshird I'g P 0.5 Secondshird I'g Refer to JIS-C5201-1 4.32 ③Test item 2 (Bending Strength): Solder tested resistor on to PC board add force in the middle down, and under load measured its resistance variance rate. D:RTT02 \ 03 \ 05=5mm RTT01 \ 06 \ 12=3mm RTT18 \ 20 \ 25=2mm Resisbar Testing circuit board Components of bend) Chilp resistor Chilp resistor	Test item 1: (1).Variance rate on resistance 1.Resistance Range: ≥ 1Ω ΔR%=±(1.0%+0.05Ω) 2.Resistance Range:<1Ω ΔR%=±(1.0%+0.001Ω) (2).No evidence of mechanical damage. No terminal peeling off. Test item 2: (1).Variance rate on resistance 1.Resistance Range: ≥ 1Ω ΔR%=±(1.0%+0.05Ω) 2.Resistance Range:<1Ω ΔR%=±(1.0%+0.001Ω) (2).No evidence of mechanical damage. No terminal peeling off and core body cracked.	Refer to iter 3. general specification
Vibration	Refer to JIS-C5201-1 4.33 The resistor shall be mounted by its terminal leads to the supporting terminals on the solid table. The entire frequency range: from 10 Hz to 55 Hz and return to 10 Hz, shall be transferred in 1 min. Amplitude :1.5 mm This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (a total of 6 hrs) Refer to JIS-C5201-1 4.22	$\begin{array}{l} \text{1.Resistance Range:} & \geq 1 \ \Omega \\ \text{0.1\% \cdot 0.5\% \cdot 1\%:} \pm (0.5\% + 0.05\Omega) \\ \text{2\% \cdot 5\%:} \pm (1.0\% + 0.05\Omega) \\ \text{2.Resistance Range:} < 1 \ \Omega \\ \text{1\% \cdot 2\% \cdot 5\%:} \pm (1.0\% + 0.001\Omega) \\ \text{No evidence of mechanical damage} \end{array}$	Refer to ite 3. general specificatio

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6.3 Environmental Test

Conditions			Specifications	
Conditions		Resistors	Jumper	
Resistance to Dry Heat Put tested resistor in chamber under temperature 155±5°C for 1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.(RTT01 for 125±3°C)		2.Resistance Range:<1Ω	Refer to item 3. general specification	
Refer to JIS-C5201-1 4.25		No evidence of mechanical damage.		
Thermal Shock Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room			Refer to iter 3. general specification	
Testing Condition		No evidence of mechanical damage.	No evidence of mechanical damage.	
Lowest Temperature	-55±5°C	No short or burned on the appearance	e.	
Highest Temperature	125±5 ℃			
Temperature-retaining time Refer to MIL-STD 202 Method 107	15 minutes each			
Loading Life in Moisture Put the tested resistor in the chamber under temperature 40±2 °C, relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.		$\pm (3.0\% + 0.1\Omega)$ $\pm (2.0\% + 0.10\Omega)$ 2.Resistance Range:<1 Ω	Refer to itel 3. general specification	
Refer to JIS-C5201-1 4.24		No evidence of mechanical damage.		
and load the rated voltage for 90 minut total 1000 hours. Then leaving the test temperature for 60 minutes, and meast variance rate.	es on, 30 minutes off, ed resistor in room		Refer to item 3. general specifications	
Refer to JIS-C5201-1 4.25		e.		
Put the tested resistor in the chamber at room temperature 25 °C. Decreasing the temperature to -55°C and keep the temperature at -55°C for 1 hour. Then load the rated voltage for 45 minutes on, and 15 minutes off. Then leaving the tested resistor in room temperature for 8±1 hours, and measure its resistance variance rate.		1.Resistance Range: ≥ 1Ω 0.1% \ 0.5% \ 1%:±(0.5%+0.05Ω) 2% \ 5%:±(1.0%+0.05Ω) 2.Resistance Range:<1Ω 1% \ 2% \ 5%:±(1.0%+0.001Ω) No evidence of mechanical damage. No short or burned on the appearance	Refer to item 3. general specifications	
	on	Μαλ. 30 μ ΙΙΙ		
<u> </u>				
-				
1 1	1,500			
Inspect for whisker formation on specil acceleration test specified in subciaus (stereo microscope) of about 40 or judgment is hard in this method, u	se 4.2, with a magnifie higher magnification. se a scanning electro	er If		
	1000 +48/-0 hours. Then leaving the te temperature for 60 minutes, and measurate. (RTT01 for 125±3°C) Refer to JIS-C5201-1 4.25 Put the tested resistor in the chamber use which shown in the following table shall consecutively. Then leaving the tested temperature for 1 hours, and measure is rate. Testing Condition Lowest Temperature Highest Temperature Highest Temperature Temperature-retaining time Refer to MIL-STD 202 Method 107 Put the tested resistor in the chamber use of the condition of the tested resistor in rome temperature for its resistance variance rate. Refer to JIS-C5201-1 4.24 Put the tested resistor in chamber under and load the rated voltage for 90 minut total 1000 hours. Then leaving the tested temperature for 60 minutes, and measurariance rate. Refer to JIS-C5201-1 4.25 Put the tested resistor in the chamber and load the rated voltage for 90 minut total 1000 hours. Then leaving the tested temperature for 60 minutes, and measurariance rate. Refer to JIS-C5201-1 4.25 Put the tested resistor in the chamber and load the rated voltage for 90 minut total 1000 hours. Then leaving the tested temperature for 60 minutes, and measurariance rate. Refer to JIS-C5201-1 4.25 Put the tested resistor in the chamber and load the rated voltage for 90 minutes of 1000 hours. Then leaving the tested temperature for 8±1 hours and 15 minutes of 1000 hours. Then leaving the tested resistor in room temperature for 8±1 hours and 15 minutes of 1000 hours. Then leaving the tested resistor in room temperature for 8±1 hours and 15 minutes of 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving the tested resistor in the chamber and 1000 hours. Then leaving th	1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance varianc rate. (RTT01 for 125±3°C) Refer to JIS-C5201-1 4.25 Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate. Testing Condition Lowest Temperature -55±5°C Highest Temperature 125±5°C Temperature-retaining time 15 minutes each Refer to MIL-STD 202 Method 107 Put the tested resistor in the chamber under temperature 40±2°C, relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measur its resistance variance rate. Refer to JIS-C5201-1 4.24 Put the tested resistor in chamber under temperature 70±2°C and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.24 Put the tested resistor in the chamber at room temperature 25°C. Decreasing the temperature to -55°C and keep the temperature at -55°C for 1 hour. Then load the rated voltage for 45 minutes on, and 15 minutes off. Then leaving the tested resistor in room temperature for 8±1 hours, and measure its resistance variance rate. Refer to MIL-R-55342D 4.7.4 Testing Condition Minimum storage temperature -55±0/-10°C Maximum storage temperature -55±0/-0°C Temperature-retaining time 10 min. Number of temperature expelse 1,500 Olnspection: Inspect for whisker formation on specimens that underwent the acceleration test specified in subciause 4.2, with a magnific (stereo microscope) of about 40 or higher magnification.	1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. (RTT01 for 125±3°C) Refer to JIS-C5201-1	

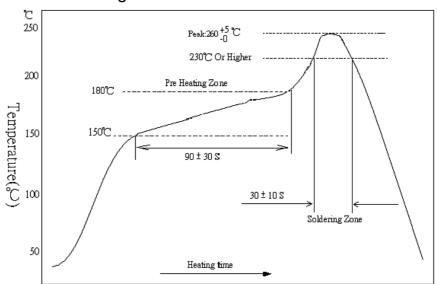
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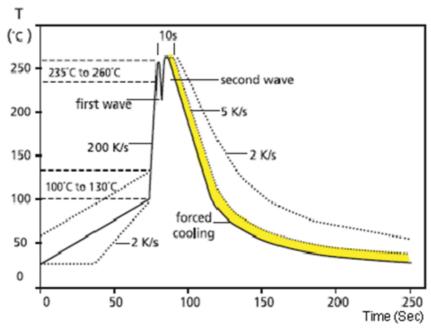
7 Recommend Soldering Method:

7.1 Lead Free Reflow Soldering Profile



Remark: The peak temperature of soldering heat is 260 +5/-0 °C for 10 seconds

7.2 Lead Free Double-Wave Soldering Profile.(This applies to 0603 size inclusive above products)



7.3 Soldering Iron: temperature $350^{\circ}\text{C} \pm 10^{\circ}\text{C}$, dwell time shall be less than 3 sec.

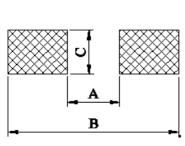
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8 Recommend Land Pattern Design (For Reflow Soldering)

Unit:mm



DIM TYPE	А	В	С
RTT01	0.3	1.0	0.4
RTT02	0.5	1.5	0.6
RTT03	8.0	2.1	0.9
RTT05	1.2	3.0	1.3
RTT06	2.2	4.2	1.6
RTT12	2.2	4.2	2.8
RTT18	3.1	5.9	3.0
RTT20	3.5	6.1	2.8
RTT25	3.8	8.0	3.5

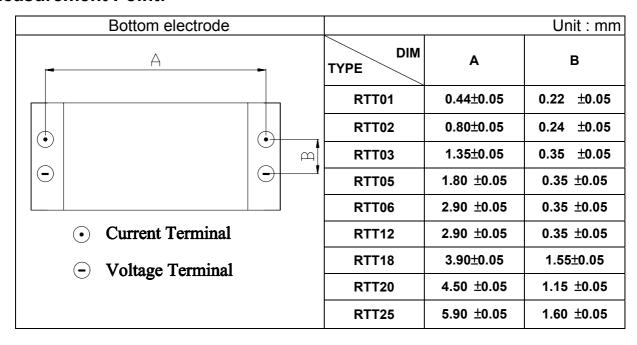
9 Plating Thickness:

9.1 Ni: \ge 2 μ m

9.2 Sn(Tin): \ge 3 μ m

9.3 Sn(Tin):Matte Sn

10 Measurement Point:



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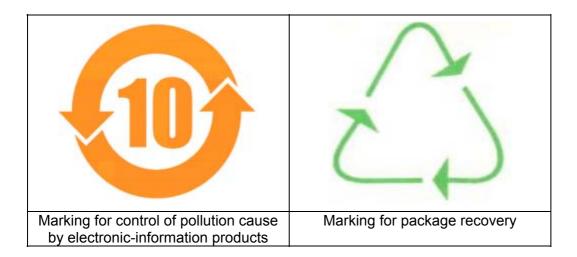
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11 Stock period:

11.1 The temperature condition must be controlled at 25±5°C, the R.H. must be controlled at 60±15%. The stock can maintain quality level in two years.

12 The carton packaged for electronic-information products is made by the symbol as follows: (For china)



13 Attachments:

13.1 Document Revise Record (QA-QR-027)

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