ZBDongle-E

Firmware Acquisition

Coordinator:

https://github.com/itead/Sonoff_Zigbee_Dongle_Firmware/tree/master/Dongle-E/NCP

Router:

https://github.com/itead/Sonoff_Zigbee_Dongle_Firmware/tree/master/Dongle-E/Router

Firmware Flashing

Use any tool that supports sending Xmodem(N), Here use SecureCRT as example.

1. Set the "Quick Connect", and connect. The port can be viewed in the Management Console.

| Protocol: | Serial | ~ | |
|--------------------------------|--------------------|-------------------------------------|--|
| The port may b | e manually entered | or selected from the list. | |
| Port: | COM7 USB-En | hanced-SERIAL CH91 \smallsetminus | |
| Baud rate: | 115200 | Flow Control | |
| <u>D</u> ata bits: | 8 | D <u>T</u> R/DSR | |
| P <u>a</u> rity: | None | | |
| Stop bits: | 1 | ~ | |
| Na <u>m</u> e of pipe: | | | |
| | | | |
| <mark>Sho<u>w</u> quick</mark> | connect on startup | Save session | |
| | | 🖌 Open in a <u>t</u> ab | |
| | | Connect | |

2. Dongle Enter Bootloader Mode.

Keep pressing the Boot button, restart the device, and release the Boot button after Dongle enters the serial port Bootloader, then enter 1.

| 🕞 Serial-COM7 (2) - SecureCRT | | | | | × |
|---|----------------------|---------------------|-----------|-----|-----|
| File Edit View Options Tra -:: # # :: :: :: :: :: | nsfer Script Tools W | /indow Help | | | |
| Sonoff v1.0.1 Sonoff v1.0.1 Gecko Bootloader v1.12.0 1. upload gbl 2. run 3. ebl info BL > begin upload CCCCCCC | 00 | | | | 4 Þ |
| Active Sessions | | | | | |
| Ready | Serial: COM7, 115200 | 10, 7 24 Rows, 80 C | ols Xterm | CAP | NUM |

3. Click "Send Xmodem(N)", select local downloaded firmware.

*Note: You should complete this step before the end of the progress bar displayed as the character "C". Otherwise, an error will be reported, and you need to re-enter 1.



4. Download completed.



5. Finally, enter "2" or reboot manually.

ZBDongle-P

Firmware acquisition

Download the Z-Stack_3.x.0 firmware of CC2652P USB Dongle from the following link:

Coordinator:

https://github.com/Koenkk/Z-Stack-firmware/tree/master/coordinator/Z-Stack_3.x.0

Router:

https://github.com/Koenkk/Z-Stack-firmware/tree/master/router/Z-Stack_3.x.0

Firmware flashing

Method 1:

Use the automatic upgrade tool "cc2538-bsl" to achieve "Auto BSL". <u>https://github.com/JelmerT/cc2538-bsl</u>

Method 2:

CC2652P USB Dongle supports serial port Bootloader to flash firmware. Use firmware flashing tools like "Flash Programmer 2" to flash the firmware.

1. Enter the serial port Bootloader

There are two ways for Dongle to enter Bootloader:

1. Manual mode

Keep pressing the Boot button, restart the device, and release the Boot button after Dongle enters the serial port Bootloader.

2. Automatically enter the serial port Bootloader through a python script

https://sonoff.tech/wp-content/uploads/2023/02/Auto-enter-bootloader.zip

Step to execute python script:

1) Download, unzip and execute the file





3) After executing the file, enter 55 55 in the serial port assistant tool, and receive the returned result 00 CC, which means that the dongle has successfully entered the Bootloader.



Note: The serial port Bootloader does not enable hardware flow control.

2. Using Flash Programmer 2 serial port flashing firmware



Note: There is no difference between the coordinator and routing firmware flashing steps.

Enable Hardware Flow Control and Generate Corresponding Firmware (optional)

If you need to enable the hardware flow control of the CC2652P USB Dongle, you need to use CCS to import the ZNP project to configure and compile the firmware that supports the hardware flow control.

Note: At present, the device cannot be used after hardware flow control is enabled, and the open source platforms have not yet supported it.

1. Import the ZNP project of CC1352P into CCS



2. Click [Browse] and select the ZNP project file under SDK:

| D | 😚 Select Search D | irectory | | | | | | | | | \times |
|----|-------------------|----------|-------------------|---|------------|-----------------------------------|-------------------------|---|---|--|----------|
| H | ← → • ↑ | > 此月 | 围脑 > Windows (C:) | > ti > simplelink_cc13x2_26x2_sdk_4_40_04_0 | 4 > exampl | les > rtos > CC1352P_2_LAUNCHXL > | ⇒ zstack → znp → tirtos | ~ | Ö | | |
| er | 组织 ▼ 新建3 | (件夹 | | | | | | | | | 0 |
| RI | 🔹 快速访问 | | 名称 | ^ 修改日期 | 美型 | 大小 | | | | | |
| A | 「三点面 | 1 | Ccs | 2021/2/25 18:15 | 文件夹 | | | | | | |
| A | I TH | | iar | 2021/2/25 18:15 | 文件夹 | | | | | | |
| a | - 1° 302 | * | ticlang | 2021/2/25 18:15 | 文件夾 | | | | | | |
| £ | 國 文档 | * | | | | | | | | | |
| В | ▶ 图片 | * | | | | | | | | | |

3. Click [Finish]

2. Configure engineering hardware flow control

1. Open the .syscfg configuration file in the ZNP project:



2. Enable serial flow control in the UART option in the .syscfg configuration file:

| \Xi Type Filter Text 🗙 🔸 | $K \leftrightarrow \Rightarrow$ Software \Rightarrow UART | | <> ■ ④ … |
|---|---|------------------------------------|-----------|
| SD 🕀 | Name | CONFIG_DISPLAY_UART | |
| SHA2 1 🥑 🕀 | Use Hardware | None | |
| U SPI 1 ♥ ↔ | Data Direction | Send and Receive | |
| E Timer | Error Callback Function | Enter a function name to enable | |
| TRNG 1 🛇 🕀 | Elow Control | | |
| UART 1 | Diag Buffer Size | | |
| UART2 (+ | hing burlet size | 32 | |
| Watchdog 🕀 | Interrupt Priority | 7 - Lowest Priority | |
| TI DRIVER APPS (2) | Software Interrupt Priority | 0 - Lowest Priority | |
| Button 2 🥑 🕀 | TX Interrupt FIF0 Threshold | 1/8 | |
| LED 2 🤡 🕀 | RX Interrupt FIFO Threshold | 4/8 | |
| INSTRUMENTATION (6) | | | |
| Bench (± | A PinMux Peripheral and Pin Configuration | | ~ |
| LogMain | UART Peripheral | Any(UART0) | * |
| Logsite (+ | TX Pin | DI013/4 (XDS110 UART) | - € |
| LoggerITM | | Connected to hardware(Un-suppress) | |
| LoggerText | DV Dia | DI012/2 (XDS110 LIART) | * A |
| TI UTILS RUN-TIME (1) | IN THE | Connected to hardware(Un-suppress) | U |
| ✓ EXECUTION GRAPH (8) | | | A |
| ExGr_LoggerBuf (+) | CTS Pin | DI019/19 (Header) | · · · · · |
| ExGr_SYSBIOS | RTS Pin | DIO18/36 (Header) | ▼ 🗄 |
| ExGr_tiutils (+) | | | |
| ExGr_XDCtools (+ | TX PIN Configuration While Pin is Not In Use | | ^ |
| ExecGraphAdapter (+) | | | |
| ExecGraphFilter | RX PIN Configuration While Pin is Not In Use | | ^ |
| ExecGraphLogger (+ | | | |
| ExecGraphSettings (+) | CTS PIN Configuration While Pin is Not In Use | | ^ |
| RPC INFRASTRUCTURE (1) | | | |
| TUTUS (2) | RTS PIN Configuration While Pin is Not In Use | | ^ |
| ISON (2) | | | |
| PTOC | Other Dependencies | | ^ |

3. Then save and compile.